

Interview Summary	Application No. 10/671,561	Applicant(s) LARSSON ET AL.	
	Examiner John H. Le	Art Unit 2863	

All participants (applicant, applicant's representative, PTO personnel):

(1) John H. Le. (3) _____.

(2) Attorney Martin Miller. (4) _____.

Date of Interview: 05 April 2006.

Type: a) ☐ Telephonic b) ☐ Video Conference
c) ☒ Personal [copy given to: 1) ☐ applicant 2) ☒ applicant's representative]

Exhibit shown or demonstration conducted: d) ☐ Yes e) ☐ No.
If Yes, brief description: _____.

Claim(s) discussed: 1.

Identification of prior art discussed: USP 5,198,746.

Agreement with respect to the claims f) ☐ was reached. g) ☐ was not reached. h) ☒ N/A.

Substance of Interview including description of the general nature of what was agreed to if an agreement was reached, or any other comments: See Continuation Sheet.

(A fuller description, if necessary, and a copy of the amendments which the examiner agreed would render the claims allowable, if available, must be attached. Also, where no copy of the amendments that would render the claims allowable is available, a summary thereof must be attached.)

THE FORMAL WRITTEN REPLY TO THE LAST OFFICE ACTION MUST INCLUDE THE SUBSTANCE OF THE INTERVIEW. (See MPEP Section 713.04). If a reply to the last Office action has already been filed, APPLICANT IS GIVEN A NON-EXTENDABLE PERIOD OF THE LONGER OF ONE MONTH OR THIRTY DAYS FROM THIS INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW SUMMARY FORM, WHICHEVER IS LATER, TO FILE A STATEMENT OF THE SUBSTANCE OF THE INTERVIEW. See Summary of Record of Interview requirements on reverse side or on attached sheet.

Examiner Note: You must sign this form unless it is an Attachment to a signed Office action.



Examiner's signature, if required

Continuation of Substance of Interview including description of the general nature of what was agreed to if an agreement was reached, or any other comments:

Examiner agrees the prior art does not teach the steps of

a) determining, for each of the interfaces a voltage phasor at the interface and a phasor of a current flowing through the interface, the measurements at the different interfaces being made essentially simultaneously, and

b) computing, from said voltage and current phasors, values of impedances constituting the equivalent circuit.

However, examiner will provide an update search.